




2 Reference Information

2-1 List of Abbreviations, Symbols and Acronyms

2-1-1 Abbreviations

Abbreviation	Definition	Abbreviation	Definition
AUTO_MENB	AUTO ENABLE (NEG.)	OSCOUNT1	24MHz CLOCK
AUTO_SOG	SINK ON GREEN ENABLE	PANEL_EN	+12V_PANEL/+5V_PANEL ENABLE
BL_EN	LCD PANEL BACK LIGHT ENABLE	PC_BLUE_IN	BLUE COLOR SIGNAL FROM PC
BRIGHT	BRIGHTNESS CONTROL	PC_GREEN_IN	GREEN COLOR SIGNAL FROM PC
DAB(7:0)	BLUE COLOR DATA (ODD) FROM IC305	PC_HSYNC_IN	HSYNC FROM PC
DAG(7:0)	GREEN COLOR DATA (ODD) FROM IC305	PC_RED_IN	RED COLOR SIGNAL FROM PC
DAR(7:0)	RED COLOR DATA (ODD) FROM IC305	PC_VSYNC_IN	VSYNC FROM PC
DBB(7:0)	BLUE COLOR DATA (EVEN) FROM IC305	PCBLUE(7:0)	BLUE COLOR DATA FROM IC101
DBG(7:0)	GREEN COLOR DATA (EVEN) FROM IC305	PCCLAMP	BUFFERED VIDEO CLAMP SIGNAL
DBR(7:0)	RED COLOR DATA (EVEN) FROM IC305	PCCLAMP1	VIDEO CLAMP SIGNAL
DDC_SCL	DDC I2C CLOCK FROM PC	PCCLK	PLL CLOCK OUT FOR IC301
DDC_SDA	DDC I2C DATA FROM PC	PCCLK2	PLL CLOCK OUT FOR IC405
DEN	LVDS DATA ENABLE	PCCLK3	PLL CLOCK OUT FOR IC110
DFSYNCB	CONTROL SIGNAL FROM IC301 TO IC305	PCGREEN(7:0)	GREEN COLOR DATA FROM ADC(IC101)
DHCLK	DOT CLOCK FOR PANEL DRIVING	PCRED(7:0)	RED COLOR DATA FROM IC101
DHS	LVDS HSYNC	PCVSYNC2	BUFFERED VSYNC
DREFCLK1	67MHz OSC CLK FOR IC305	RESETB	RESET (NEG.)
DV_BLU	OSD BLUE DATA	RGB_HSYNC	HSYNC FOR MICOM(IC401)
DV_FBK	OSD ENABLE	RGB_VSYNC	VSYNC FOR MICOM(IC401)
DV_GRN	OSD GREEN DATA	SCL	I2C CLOCK
DV_RED	OSD RED DATA	SCSB	IC305 ENABLE
DVACTIV1B	HSYNC FOR OSD (NEG.)	SDA	I2C DATA
DVCLK	DOT CLOCK FOR OSD	SOG_CSYSN	COMPOSITE SYNC FROM SOG
DVS	LVDS VSYNC	SOURCE_PC	SELF RASTER CHECK SIGNAL
DVSYNCB	VSYNC FOR OSD (NEG.)	SPI_MISO	SERIAL INPUT DATA CONTROL
FSD(47:0)	VIDEO DATA BETWEEN IC301 AND IC302,IC303,IC304	SPI_MOSI	SERIAL OUTPUT DATA CONTROL
HSYNC_PLL	HSYNC FOR PLL	SPI_SCK	SERIAL CLOCK
INVERT	INTERLACE CONTROL SIGNAL	SW_REG_ENB	POWER ON/OFF CONTROL
KEY1	FUNCTION KEY SIGNAL1 TO MICOM	VAIL_CSB	IC301 ENABLE
KEY2	FUNCTION KEY SIGNAL2 TO MICOM	VCBLNKB	CONTROL SIGNAL FROM IC305 TO IC301
LED	LED ON	VCC	DC 5V FOR MICOM(IC401)
LVDS_DATA	DATA OUTPUT FROM IC310, IC311	VCLREQB	CONTROL SIGNAL FROM IC305 TO IC301
LVDS_EN	LVDS ENABLE	VGABLU(7:0)	NC
M_HSYNC	BUFFERED MICOM OUTPUT HSYNC	VGAGREEN(7:0)	NC
M_HSYNC1	MICOM OUTPUT HSYNC	VGARED(7:0)	NC
M_VSYNC	BUFFERED MICOM OUTPUT VSYNC	VGBBLU(7:0)	BLUE COLOR DATA FROM IC301
M_VSYNC1	MICOM OUTPUT VSYNC	VGBGREEN(7:0)	GREEN COLOR DATA FROM IC301
OSCOUNT	BUFFERED 24MHz CLOCK	VGBRED(7:0)	RED COLOR DATA FROM IC301

2-1-2 Symbols

-  Hot Ground
 Cold Ground
 Provides special safety considerations

2-1-2 Acronyms

Acronym	Definition	Acronym	Definition
ABL	Automatic Brightness Limits	H/V	Horizontal/Vertical
AC	Alternating Current	HV	High Voltage
ACL	Automatic Contrast Limit	I/O	Input/Output
AFC	Automatic Frequency Control	IC	Integrated Circuit
ANSI	American National Standards Institute	LED	Light Emitting Diode
CMOS	Complementary Metal Oxide Semiconductor	MAC	Macintosh
CRT	Cathode Ray Tube	MOFA	Mask Outside Frame Assembly
DC	Direct Current	OCP	Over Current Protection
DDC	Data Display Channel	OP AMP	Operational Amplifier
DF	Dynamic Focus	OSD	On Screen Display
DMM	Digital Multimeter	P-P	Peak to Peak
DPMS	Display Power Management Signaling	PCB	Printed Circuit Board
DVM	Digital Voltmeter	PLL	Phase Locked Loop
DY	Deflection Yoke	PWM	Pulse Width Modulation
EEPROM	Electrically Erasable and Programmable Read only Memory	SMPS	Switch Mode Power Supply
ESD	Electrostatically Sensitive Device	SVGA	Super Video Graphics Array
ESF	Electronic Static Field	TP	Test Point
FBT	Flyback Transformer	UL	Underwriters Laboratories
FET	Field Effect Transistor	USB	Universal Serial Bus
FH	Horizontal Frequency	VESA	Video Electronics Standard Association
FS	Fail Safe	VGA	Video Graphics Array
FV	Vertical Frequency	VR	Variable Register
GD	Geometric Distortion	W/B	White Balance